



UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Timothy Gerard Richter et al)
Serial No.: 09/918,993) Group Art Unit:
Filed: July 31, 2001) Before the Examiner:
For: Circuit Breaker Mechanism Modeling)

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §§ 1.56, 1.97 AND 1.98

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

I certify that this correspondence is being deposited on October 31, 2001 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Assistant Commissioner for Patents, Washington, DC 20231.

Amy B. Mantie

(Name of Person Mailing Paper)

Amy B. Mantie 10-31-01
Signature Date

In compliance with the duty to disclose, applicants submit herewith a copy of each publication of which those designated by 37 CFR § 1.56 are aware; the publications are listed on attached form PTO-1449.

Consideration of this Information Disclosure Statement is respectfully requested, since the Examiner may consider the information provided herein material to the patentability of the subject application as defined in 37 CFR § 1.56.

Since this Information Disclosure Statement is being filed within three months of the filing date of the subject application, no fee or certification under 37 CFR § 1.97 (e) is required.

However, in the event the Commissioner of Patents deems that any fee is required under 37 CFR §§ 1.16 and 1.17 in connection with this application, applicants' attorneys authorize that such fee be charged to Deposit Account No. 06-1130.

Respectfully submitted,

Timothy Gerard Richter et al

CANTOR COLBURN LLP
Applicants' Attorneys

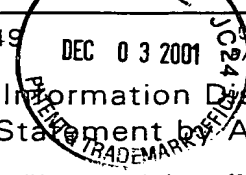
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CERTIFICATE OF MAILING BY FIRST CLASS MAIL (37 CFR 1.8)			Docket No.
Applicant(s): Timothy Gerard Richter et al			GEN-0261
Serial No. 09/918,993 ✓	Filing Date July 31, 2001	Examiner	Group Art Unit
Invention: Circuit Breaker Mechanism Modeling			
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<p>I hereby certify that this <u>Information Disclosure Statement with Form PTO-1449</u> (Identify type of correspondence)</p> <p>is being deposited with the United States Postal Service as first class mail in an envelope addressed to: The Assistant Commissioner for Patents, Washington, D.C. 20231 on <u>October 31, 2001</u> (Date)</p> <p><u>Amy B. Mantie</u> (Typed or Printed Name of Person Mailing Correspondence)</p> <p><u>Amy B. Mantie 10-31-01</u> (Signature of Person Mailing Correspondence)</p>			
<p>Note: Each paper must have its own certificate of mailing.</p>			

Form PTO-1449 (Modified)		 <p>Information Disclosure Statement by Applicant (Use several sheets if necessary)</p>			ATTY. DOCKET NO. GEN-0261/41PR-7843		SERIAL NO. 09/918,993	
		APPLICANT Timothy Gerard Richter et al						
		FILING DATE July 31, 2001				GROUP		

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		PATENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		4,245,318	1/13/81	Eckart et al.	364	481	
		4,733,032	3/22/88	Pardini	200	144	
		4,868,770	10/19/89	Smith et al.	364	578	
		5,046,024	9/3/91	Smith et al.	364	518	
		5,092,780	3/3/92	Vlach	434	433	

FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION							
		DOCUMENT NUMBER	PUBLICATION DATE*	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION YES NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc. **)	
	Avant! Home Page, printed from http://www.analogv.com/home.htm on May 22, 2000.
	Avant! Modeling Product Page, printed from http://www.analogv.com/Products/Modeling/modeling.htm - Hardware Description Language on May 10, 2000.
	Avant! Modleing Tools, printed from http://www.analogv.com/Products/Modeling/modeling-Tools.htm on May 10, 2000.
	Avant! Analysis Product Page, printed from http://www.analogv.com/Products/analysis/analysis.htm on May 10, 2000.
	Analogy® SaberDesigner™ Product Brochure, printed from http://www.analogv.com/Products/DataSheets/prodbro.pdf on May 10, 2000
	Analogy® Saber® Solutions Saber-IC™ Pro Brochure, printed from http://www.analogv.com/Products/DataSheets/ss_saber_icpro.pdf on May 10, 2000.
EXAMINER	
DATE CONSIDERED	

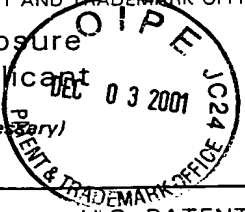
*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

(Form PTO-1449 [6-4])

* Use Publication date indicated on the patent or published application. 37 CFR 1.98(b).

** "The place or publication refers to the name of the journal, magazine or other publication in which the article was published...." Notice of January 9, 1992, 1135 O.G. 13 to 25, at 20.

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Information Disclosure Statement by Applicant (Use several sheets if necessary)				APPLICANT Timothy Gerard Richter et al			
(37 cfr 1.98(b))				FILING DATE July 31, 2001		GROUP	



U.S. PATENT DOCUMENTS							
EXAMINER INITIAL	PATENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
	5,168,261	12/1/92	Weeks	340	515		
	5,199,103	3/30/93	Smith et al.	395	140		
	5,548,539	8/20/96	Vlach et al.	364	578		
	5,418,677	5/23/95	Engel	361	25		
	5,872,722	2/16/99	Oravetz et al.	364	528.27		
	5,946,474	8/31/99	Skogby	395	500		
	5,963,724	10/5/99	Mantooth et al	395	500		

FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION							
DOCUMENT NUMBER	PUBLICATION DATE*	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION		
					YES	NO	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.**)		
		Analogy® SaberScope™ Graphical Waveform Analyzer Brochure, printed from http://www.analogy.com/Products/DataSheets/sscope.pdf on May 10, 2000.

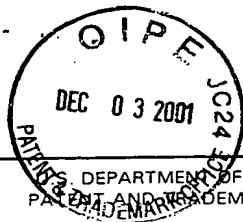
EXAMINER	DATE CONSIDERED
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EXAMINE R INITIAL		PATENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION							
		DOCUMENT NUMBER	PUBLICATION DATE*	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION YES NO
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.**)							
		Annis, Jeffrey R. and Bradley, Allen; Rockwell Automation; "Monte-Carlo Analysis and System Simulation of a Solid State Overload Relay to Evaluate Design Robustness"; Assure Meeting Paper, March 12-14, 1997.					
		Morel, Robert; Cahier Technique no. 154, Merlin Gerin; "Low Voltage Circuit-Breaker Breaking Techniques; December, 1994.					
		Boillot, Louis and Mignée, Jean; Cahier Technique no. 140, Merlin Gerin; "Simulation De Comportment En Court-Circuit Des Disjoncteurs Basse Tension Limiteurs"; September, 1987. (Translation not provided)					
		Schueller, Pierre; Cahier Technique no. 103, Merlin Gerin; "Coupure En BT Par Limitation Du Courant"; October, 1983. (Translation not provided)					
EXAMINER				DATE CONSIDERED			
*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

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